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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Dwarakanath, *et al.*

Attorney Docket: ENP-003

Filed: January 29, 2004

Examiner: TBD

Serial No.: 10/767,540

Art Unit: 2838

For: Driver for a Power Converter and a Method of Driving a Switch Thereof

Mail Stop: Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

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The Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08a & 08b that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b)(3), before the mailing of a first Office action on the merits.

Respectfully submitted,

Glenn W. Boisbrun
Attorney for Applicants
Reg. No. 39,615

August 6, 2004

Date

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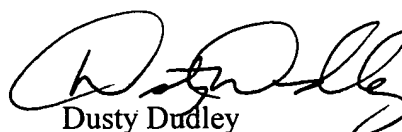
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Date of Deposit: August 6, 2004

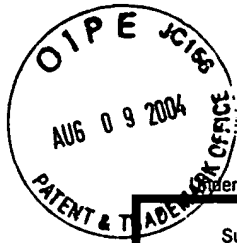
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Information Disclosure Statement (1 page)
Form PTO/SB/08a & 08b (2 pages) citing 11 references
Copies of 4 References
Return Postcard

Respectfully submitted,


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Legal Assistant

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PTO/SB/08a (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known

Application Number	10/767,540
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Filing Date	01-29-2004
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First Named Inventor	Dwarakanath
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Art Unit	2838
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Examiner Name	TBD
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Attorney Docket Number	ENP-003
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Sheet

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of

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U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

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**Examiner
Signature**

Date
Considered

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**INFORMATION DISCLOSURE
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(Use as many sheets as necessary)

Sheet

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of

2

Complete if Known

Application Number

10/767,540

Filing Date

01-29-2004

First Named Inventor

Dwarakanath

Art Unit

2838

Examiner Name

TBD

Attorney Docket Number

ENP-003

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	8	BETANCOURT-ZAMORA, R.J. <i>et al.</i> , "A 1.5 mW, 200 MHz CMOS VCO for Wireless Biotelemetry," First International Workshop on Design of Mixed-Mode Integrated Circuits and Applications, Cancun, Mexico, pp. 72-74, July, 1997.	
	9	GOODMAN, J. <i>et al.</i> , "An Energy/Security Scalable Encryption Processor Using an Embedded Variable Voltage DC/DC Converter," IEEE Journal of Solid-State Circuits, Vol. 33, No. 11 (November 1998).	
	10	HOROWITZ, P., <i>et al.</i> , "The Art of Electronics," Second Edition, 1989, pp. 288-291, Cambridge University Press, Cambridge, MA.	
	11	LOTFI, A.W., <i>et al.</i> , "Issues and Advances in High-Frequency Magnetics for Switching Power Supplies," Proceedings of the IEEE, June 2001, Vol. 89, No. 6, pp. 833-845.	

Examiner
Signature

Date

Considered

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